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**BOX AF**

RESPONSE UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
GROUP 2816  
PATENT APPLICATION



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q65962

Katsuji KIMURA

Appln. No. 09/940,472

Group Art Unit: 2816

Confirmation No. 4891

Examiner: Minh T. NGUYEN

Filed: August 29, 2001

For: LINEAR VOLTAGE SUBTRACTOR/ADDER CIRCUIT AND MOS DIFFERENTIAL  
AMPLIFIER CIRCUIT WHEREFOR

RESPONSE UNDER 37 C.F.R. § 1.116

ATTN: BOX AF  
Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the Office Action dated January 27, 2003, please consider the remarks as  
submitted herewith.

#13 / response  
(N6)  
5/22/03  
Shu Th

PLEASE DO NOT

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